

multiply the contour component sampled by means of the contour pick-up unit for outputting the product thereof, and an adder for adding the contour component output from the coefficient control unit to the input video signal for outputting a contour-emphasized video signal.

6. A contour emphasizing circuit according to Claim 5, wherein the coefficient control unit comprises n number of multipliers for multiplying the contour component sampled by using the contour pick-up unit by the coefficient corresponding to one of said n number of luminance levels for outputting the product thereof, n number of AND gates respectively connected to the output sides of the n number of multipliers for using the signal interpreted by said decoder as gate control signal, and an OR gate connected to the output sides of the n number of AND gates.

7. A contour emphasizing circuit according to Claim 5, wherein the level judging unit comprises a decoder for discriminating the luminance level of an input video signal on the basis of m-bit data of upper luminance levels to divide the luminance level into n number ($n = 2^{m-1}$) of luminance levels, [whose maximum values range from 1 or less, 1/2 or more, 1/2 or less, 1/4 or more, 1/4 or less, 1/8 or more, ..., to 0 or more, to interpret whether the luminance level of an input video signal corresponds to which of the n number of luminance levels.

8. A contour emphasizing circuit according to Claim 7, wherein the level judging unit comprises the decoder for interpreting whether the luminance level of input video signal corresponds to which of four luminance levels, and the coefficient control unit comprises four multipliers for multiplying the contour components sampled by using the

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Decoder
Output
Data